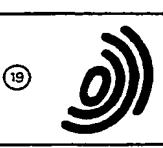


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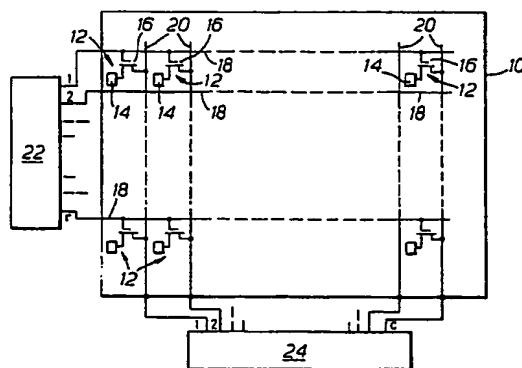
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(54) **Fingerprint sensing.**

(57) To obtain fingerprint pattern information the capacitances between individual sense elements in an array and respective finger surface portions overlying the elements are sensed to provide an electronic representation of the variation of the capacitances as determined by the spacing from the array of the ridge and trough pattern of the fingerprint. A fingerprint sensing device comprises a row/column array of actively addressed sense elements (12) connected to sets of row and column conductors (18,20) and comprising sense electrodes (14) covered by insulating material (32) over the surface (34) of which a finger (37) is placed, either directly thereon or on respective conductive pads overlying the sense electrodes. The capacitance at each sense element is sensed for example by applying a potential to the sense electrode and measuring charging characteristics through a sensing circuit (24). Each sense element includes a switching device (16), e.g. a TFT for active addressing. In a fingerprint recogni-

tion system the sensing device output is analysed and characterised data is compared with stored data for identification or verification purposes.

Fig.1.



This invention relates to fingerprint recognition systems and particularly fingerprint sensing devices for use in such systems.

Fingerprint recognition has been suggested for many applications, ranging from high security uses such as access control for buildings, computers or the like to low security uses such as replacements for conventional locks and keys. The main advantages of such systems are that they are easy and convenient to use, avoid the need for keys, personal identification numbers and the like, and should be less susceptible to fraudulent use. The sensing device is an important part of the system and the quality of the representation of the fingerprint that the device picks up will affect recognition capability and the amount of preprocessing required for verification.

Conventional forms of fingerprint sensing devices rely on optical detection methods. A simple optical sensing method can be open to fraudulent use by presenting a photographic image of a fingerprint. A more secure and common approach is to use a glass prism with frustrated total internal reflection. Light is directed through one face, reflected at a second face and passes out at the third face. A finger is placed on the second face and at the points where the finger is in contact with the glass, i.e. at the ridges of the fingerprint, reflection no longer takes place. Light continues to be reflected at regions where troughs of the fingerprint pattern are present. Light output from the third face is picked up by an image sensor. A binary image, in which those parts of the fingertip in contact with the glass show as black and the rest as white, can therefore be obtained. Such an optical sensing device has disadvantages however. For example, the device is comparatively bulky. Also it is essential that the contact surface be maintained clean and free of dirt or grease. Moreover, problems can occur because some people have drier fingers than others and because most people's fingers become drier in cold weather. A dry finger actually touches the glass at many fewer points than a moist finger with the result that the lines of the fingerprint appear as rows of small dots so that more image processing becomes necessary.

An alternative kind of fingerprint sensor was proposed in US-A-4353056 which uses a capacitive sensing approach. The described sensor has a two dimensional, row and column, array of capacitors, each comprising a pair of spaced electrodes, carried in a sensing member and covered by an insulating film. The sensors rely upon deformation to the sensing member caused by a finger being placed thereon so as to vary locally the spacing between capacitor electrodes, according to the ridge/trough pattern of the fingerprint, and hence the capacitance of the capacitors. In one arrange-

ment, the capacitors of each column are connected in series with the columns of capacitors connected in parallel and a voltage is applied across the columns. In another arrangement a voltage is applied to each individual capacitor in the array. Sensing in the respective two arrangements is accomplished by detecting the change of voltage distribution in the series connected capacitors or by measuring the voltage values of the individual capacitances resulting from local deformation. To achieve this, an individual connection is required from the detection circuit to each capacitor.

While the described sensor may not suffer from the problems associated with the kind of sensor employing an optical sensing technique, it suffers from its own problems. For example, because the sensor relies in operation on deformation and requires the use of resilient materials, problems with durability and reliability may be experienced. Moreover, the need to provide a respective connection to each individual capacitor in the array means that a very large number of connecting lines is necessary. This creates difficulties, both in the fabrication of the sensing member and its interconnection with the detection circuit. In practice the large number of connections would likely also cause operating difficulties due to stray capacitances.

It is an object of the present invention to provide an improved sensing device which uses a capacitive sensing approach but which avoids at least to some extent the aforementioned problems.

According to a first aspect of the present invention there is provided a fingerprint sensing device comprising an array of sense elements which are connected to a drive circuit and which each comprise a sense electrode, the sense electrodes being covered by dielectric material determining a sensing surface over which a finger whose print is to be sensed is placed, characterised in that each sense element includes a respective switching device connected to its sense electrode and is actively addressable by means of the drive circuit controlling operation of the switching device to apply a predetermined potential to the sense electrode, and in that the sensing device further includes sensing means for sensing a capacitance produced by individual finger surface portions in combination with respective sense electrodes when a finger is placed over said sensing surface.

Active addressing of the sensing elements, made possible by the provision of a switching device in each sensing element, greatly simplifies driving of the array and allows a number of addressing conductors required to be considerably reduced, as will become apparent. Such a sensing device can also be produced in a very compact form, and therefore is easily accommodated in

various equipment.

In use, the presence of finger surface portion overlying a sense electrode produces a respective capacitor whose capacitance is sensed. Assuming the sensing surface is constituted simply by the surface of the dielectric material, then if a finger is placed over the sensing surface the ridges of the fingerprint may be in contact with or at least close to that surface whereas the troughs are spaced further away. A capacitor is then formed by each sense electrode in combination the respective overlying portion of the finger surface, with the electrode and finger surface portion, the latter being at ground potential, constituting respective capacitor plates. In the case of the finger surface portion being a ridge which contacts the surface the capacitor plates are separated by the thickness of the dielectric layer whereas, in the case of the finger surface portion being a trough, the plates are separated by the combined thickness of the dielectric layer and the air gap between the finger surface portion and the surface of the dielectric layer. A pattern of capacitor values is therefore obtained over the area of the array representative of the variation in distance of the skin surface from the dielectric layer surface, and thus in accordance with the three-dimensional ridge pattern of the fingerprint. No deformation of the sense element array is required. By measuring the variation of these capacitances an electronic representation or image of the fingerprint pattern can be obtained. Because this representation is indicative of the three-dimensional form of the fingerprint pattern, as compared with merely a two-dimensional representation provided by the aforementioned optical kind of sensing device. At the same time, the capacitive sensing approach utilised means that the effects of dirt or grease present in normal circumstances on the sensing surface or the use of comparatively dry fingers will be less significant.

An array of discrete electrodes may be provided over the surface of the dielectric material, each of which substantially overlies a respective sense electrode. In this arrangement, each additional electrode together with its associated sense electrode constitute capacitor plates separated by the dielectric material. Different capacitance values are obtained depending on whether or not a fingerprint ridge contacts the additional electrode, thereby grounding it. Such a device therefore provides basically a standard, predetermined, capacitance value wherever contacting fingerprint ridges are present. Where troughs overlie sensing electrodes the capacitance values obtained are again determined by the spacings. By detecting simply where these predetermined capacitances exist a kind of binary "image" of the fingerprint, similar to those provided by optical sensing devices, can easily be

produced, if desired.

The drive circuit is preferably arranged to apply at regular intervals a predetermined potential to the sense electrode. Between successive applications of a predetermined potential the charge on the sense electrodes may be removed or reduced, for example by means of a grounded resistor, or alternatively the value of the predetermined potential may be changed for successive applications. Because each sense electrode forms part of a capacitor in the presence of a finger, the magnitude of the charge flowing into the capacitor depends on the size of the capacitor, which is determined by the spacing of the finger surface from the sensor surface. In order to provide an indication of the capacitor size the device may conveniently include means, for example charge sensitive amplifiers, for sensing the charging current to each of the electrodes and generating a signal output in accordance therewith can be used in subsequent signal processing. It is envisaged, however, that other ways of sensing the capacitor values could be employed.

The switching device of each sense element may comprise a three terminal device such as a field effect transistor whose source and drain are connected respectively to the drive circuit via a sense conductor and the associated sense electrode and which is controlled by means of a select, or gating, signal applied by the drive circuit to its gate via an address conductor. The sense elements are preferably in a row and column array and in this case the gates of the transistors associated with the sense elements of one row share a respective common address conductor and the sources of the transistors associated with one column share a respective common sense conductor. The sense elements can then be addressed conveniently a row at a time to build up a complete "image" of the capacitor values. In these respects, the sensing device shares similarities with active-matrix addressed display devices with regard to the addressing technique employed. Moreover, the sensing device may conveniently be fabricated using the same kind of matrix switching technology developed for such display devices using thin film deposition and photolithographic definition processes to form electrodes, addressing conductors and thin film transistors on an insulative substrate. Alternatively the sensing device may be fabricated using a semiconductor wafer and integrated circuit technology. In both cases it will be appreciated that the sensing device is considerably less bulky than conventional devices.

The dielectric material, comprising any suitably insulative material such as, for example, silicon nitride or a polyimide, may conveniently be provided as a continuous layer, preferably of uniform

thickness, over the array of sense electrodes and intervening spaces following their formation. Further conductors may be provided on the surface of the dielectric material remote from the sense electrodes and extending over regions between the sense electrodes, for example as lines or in a grid form, which conductors are grounded in order to improve the electrical contact to the finger surface.

According to a second aspect of the present invention there is provided a fingerprint recognition system comprising a sensing device in accordance with the first aspect of the invention, means responsive to an output from the sensing means of the device to provide characteristical data of a sensed fingerprint, and means for comparing said characteristical data with stored characteristical data for one or more fingerprints. An output can be obtained from the sensing device comparable to the video output provided by an image sensor in an optical sensing device. Accordingly, and as will be apparent to skilled persons, components of the system, other than the sensing device, can be generally of the kind employed in systems using optical sensing devices. The characteristical data in accordance with standard practice may take the form of information regarding the orientation of ridge lines and relative positions of minutiae, that is the ending and bifurcations of the lines. The processing of information obtained from the sensing device to produce and compare characteristical data can follow known schemes and techniques. In this respect reference may be made for example to EP-A-0343580, the paper by C. J. Elliott entitled "Automatic Fingerprint Recognition" in Parallel Processing : State of the Art Report published in 1987 by Pergamon Infotech, or the paper by Fukue et al entitled "Fingerprint Verification System-Verification Algorithm" published in IEEE TENCON 87, Seoul, 1987 at pages 71-75. Because the sensing device of the invention can provide information of the three dimensional profile of a fingerprint improved accuracy of identification or verification can be obtained by making use of topological features in addition to the spatial positions of minutiae, although of course use may be made only of information in respect of the two-dimensional ridge patterns to simplify the processing necessary if less accuracy is acceptable.

Embodiments of fingerprint sensing devices, fingerprint recognition systems and methods used therein in accordance with the invention will now be described, by way of example, with reference to the accompanying drawings, in which:-

Figure 1 is a simplified schematic diagram of an embodiment of sensing device in accordance with the invention showing an array of sense elements together with associated addressing circuitry;

Figure 2 shows schematically the equivalent circuit for a typical sense element of the device, comprising a sense electrode and associated switching device;

Figure 3 is a schematic cross-sectional view through a part of the sensing device illustrating its operation;

Figure 4 illustrates graphically the relationship between capacitance and finger surface distance for a typical sense electrode of the device;

Figures 5a and 5b shows respectively parts of two alternative sensing circuits of the sensing device;

Figure 6 illustrates typical waveforms present in operation of the sensing device;

Figures 7a and 7b show respectively schematically in plan two modified forms of the sensing device;

Figure 8 is a schematic cross-sectional view through a part of another embodiment of sensing device in accordance with the invention; and Figure 9 is a schematic block diagram of a fingerprint recognition system incorporating a sensing device.

It should be understood that the Figures are merely schematic and are not drawn to scale. In particular certain dimensions such as the thickness of the layers or regions may have been exaggerated whilst other dimensions may have been reduced. It should also be understood that the same reference numerals are used throughout the Figures to indicate the same or similar parts.

Referring to Figure 1, the sensing device comprises an active matrix addressed sensing pad 10 having a X-Y array of sense elements consisting of r rows (1 to r) with c sensing elements, 12 in each row. Only a few rows and columns are shown for simplicity. In practice there may be around 300 rows and 200 columns of regularly-spaced sense elements occupying an area of approximately 2 cms by 3cms.

Referring also to Figure 2, each sense element of the array comprises a sense electrode 14 connected to an active device, which in this embodiment comprises a three terminal switching device 16 in the form of a field effect transistor (FET). The X-Y array of sense elements is addressed via sets of row (selection) and column (sensing) address conductors 18 and 20 with individual sense elements being located at respective intersections of the conductors. All sense elements in the same row are connected to a respective common row conductor 18 and all sense elements in the same column are connected to a respective common column conductor 20. The row conductors 18 are connected at their one end to a row driver circuit, generally referenced at 22, and the column conductors 20 are connected at their one ends to a

sensing circuit, 24.

As can be seen in Figure 2, the gate and source of the FET 16 of a sense element are connected respectively to a row conductor 18 and a column conductor 20. The FET's drain electrode is connected to the sense electrode 14.

Fabrication of the sense elements 12 and address conductors 18 and 20 of the pad 10 is based on technology used in active matrix addressed display devices, such as liquid crystal display devices. This technology is now well established as a means for producing large area active matrix arrays and as such it is considered unnecessary to describe here in detail the methods by which the sensing device can be fabricated. Briefly, the method typically involves the deposition and definition by photolithographic processes of a number of layers on an insulating substrate. The electrodes 14 and sets of address conductors 18 and 20 can be formed of metal and the FETs 16 can be formed as amorphous silicon or polycrystalline silicon thin film transistors (TFTs) using an appropriate, e.g. glass or quartz, substrate.

An example of one form of construction is shown schematically in Figure 3 which is a cross-section through a representative part of the pad 10 comprising three complete sense electrodes 14. The TFT structures, which have been omitted for clarity, are formed on a glass or quartz substrate 30, by depositing a layer of amorphous or polycrystalline silicon material over the substrate and defining this layer to leave individual islands which eventually form the TFT's channels. Similarly a layer of insulating material, for example silicon nitride, is deposited and defined to form the gate insulator layers of the TFTs. The electrodes 14, comprising regularly spaced and equally sized rectangular pads, and the set of address conductors 20 extending therebetween are defined from a deposited metal layer. Integral extensions of the electrodes 14 and conductors 20 respectively form the drain and source contacts of the TFTs. Further insulating material is provided, over the conductors 20 at least at the regions where they are to intersect the address conductors 18. The set of conductors 18, not visible in Figure 3, is then formed from a deposited metal layer with each conductor extending between adjacent rows of electrodes 14 and having integral extensions along its length which overlie respective semiconductor islands and serve as gate electrodes of the TFTs. The resulting structure is similar to the active matrix structure of a display device with the sets of address conductors 18 and 20 and sense electrodes 14 being analogous respectively to the sets of gating and signal conductors and display element electrodes of a display device. Fabrication is, however, made simpler in view of the fact that metal is used for the

electrodes 14 rather than transparent conductive material, such as ITO, which is necessary for display element electrodes in a display device.

To complete the structure of the sensing device, an insulating film 32, for example of silicon nitride or polyimide, is deposited completely over the structure on the substrate 30 to provide a continuous sensing surface 34 spaced from, and substantially parallel to, the substrate surface.

The physical dimensions of the sense electrodes are chosen in accordance with the desired resolution characteristics in fingerprint sensing. By way of example, the sense electrodes may have a pitch of around 100 micrometres in both the row and column directions. The thickness of the insulating film 32 is selected taking into account the particular material used for this film. For example, in the case of a material having a relative permittivity of approximately 4, a film thickness of around 4 micrometres is chosen.

In operation of this sensing device, a finger whose print is to be sensed is placed on the sensing surface 34. Actual, or close, physical contact with the surface 34 then occurs at the ridges of the finger surface, as illustrated in Figure 3 where one ridge 36 of part of the finger surface 37 is depicted. Troughs in the finger surface adjacent to the ridges are spaced from the surface 34 by a considerably greater distance. The ridged finger surface is therefore spaced from the array of electrodes 14 by a minimum distance determined by the thickness of the thin insulating film 32. Each sense electrode 14 and the respective overlying portion of the finger surface form opposing plates of a capacitor 35, as depicted by dotted lines in Figure 3, with the plate constituted by the finger surface portion being at ground potential. The intervening material of the insulating film 32, and air gap, between the finger surface portion and the sensing surface 34 if present, provide the capacitor dielectric. The capacitances of these individual capacitors varies as a function of the spacing, d in Figure 3, between the finger surface and the sensing surface 34, with the larger capacitances occurring where the finger surface ridges are in contact with surface 34 and the smaller capacitances occurring where the troughs in the finger surface overlie the sense electrodes 24.

This variation in capacitance is illustrated in Figure 4 in which the relationship between the capacitance C_x in picofarads per square millimetre, of a capacitor 35 and the spacing d in micrometres is shown graphically for the case in which the insulating film is of a material having a relative permittivity of 4 and is 4 micrometres in thickness. The variation in capacitances produced over the array of sensing elements 12 of the pad 10 by a fingerprint ridge pattern thus constitutes in effect

an electronic "image" of the three dimensional form of the fingerprint surface. These capacitances are sensed within the sensing device and an output indicative of the variation, and hence the three-dimensional profile of the fingerprint, is provided. The three dimensional ridge pattern of a fingerprint is reproduced in the form of an electronic image by monitoring the capacitance variation between the individual sense electrodes in the array and the ridged finger surface. Because the variation in capacitances is determined by the three dimensional form of the finger any attempt at fraudulent use by producing a dummy finger is made very difficult.

Sensing the capacitance variation between the various sense elements 12 in the array is accomplished as follows. Each sense element is addressed through its associated row (selection) and column, (sensing), conductors 18 and 20. A gating pulse applied to a row conductor 18 by the row driver circuit 22 turns on the FETs 16 of all sense elements 12, in the row of elements associated with the row conductor. A predetermined potential of around 10 volts is simultaneously applied by the circuit 24 to all column conductors 20 so that upon the FETs 16 being turned on the capacitors 35 associated with the sense elements 12 of that row are charged to the potential of the column conductors. The charging current for the capacitors flows up the column conductors 20 and is sensed by an appropriate amplifier within the circuit 24. The magnitude of the charge flowing into each capacitor 35 depends on the size of the capacitor. Therefore, by measuring the charging currents in each column conductor 20 the size of each capacitor can be assessed. This procedure is repeated for each row of sensing elements of the array in turn sequentially so that a complete "image" of the capacitor characteristics is built up following the addressing of all rows in the array in one complete field period.

Figures 5a and 5b respectively illustrate two alternative sensing circuits which may be used to sense the charging characteristics of the capacitors. Figure 5a shows a part of current sensing circuit configuration for three adjacent column conductors 20. The column conductors 20 are connected to respective current amplifiers 40 with resistive feedback whose outputs are supplied to sample and hold circuits 41. The bias condition of the amplifiers sets the aforementioned predetermined potential level on the column conductors 20. These circuits 41 are operated simultaneously, and in synchronism with the gating pulse applied to a row conductor 18, by means of a sampling pulse supplied along a common line 42. The analogue outputs of the circuits 41 are switched in succession by means of a shift register 45 operating

switches 46 in sequence to provide a serial output of pulses along line 47 whose magnitudes are indicative of the instantaneous current values in each of the conductors 20.

Figure 5b illustrates a charge amplifier circuit configuration, the part shown serving two adjacent column conductors 20. In this circuit, the column conductors 20 are connected to charge amplifiers 50 with capacitive feedback whose analogue outputs are similarly switched in succession by means of a shift register 45 operating switches 46 to provide on output line 47 a serial train of pulses whose magnitude is indicative of the charge flow in each column conductor. The charge amplifiers 50 are reset in the period between addressing successive rows of sensing elements by a reset pulse applied to a reset line 51 which operates switches 52 to discharge the shunt capacitors of the amplifiers.

In order to allow several readings of the capacitance image of the fingerprint, or readings of different fingerprints in consecutive operations, it is necessary to ensure that the charge on the electrodes 14 is removed or reduced before the sense elements are addressed again. This may be achieved by incorporating a resistor in each sense element connected between the sense electrode 14 and either a grounded conductor common to all sense elements of a respective row or to the next adjacent row conductor 18. Such an arrangement is shown in dotted form in Figure 2 with the resistor and additional adjacent row conductor being referenced 15 and 17 respectively. The resistors may comprise suitably doped semiconductor material used for fabricating the TFTs.

Other approaches may however be employed. The predetermined voltage applied to the column conductors can be changed between two different levels on successive read cycles. The bias condition of the amplifiers can again be used to provide one of these levels. The other level may be provided by a means of a switch inserted between the conductor 20 and its associated amplifier 40 or 50. Alternatively, an intermediate reset cycle could be included in the addressing scheme.

All these approaches ensure that each time the sense elements are addressed some charging of the capacitors occurs so that the charging currents can be used to determine their capacitance.

Typical waveforms present in operation of the sensing device are shown, by way of example, in Figure 6. More particularly three sets, A, B and C, of waveforms are shown for different modes of operation where set A is for the version in which a resistor 15 is included in each sense element, set B is for a version in which the column voltage is inverted between successive reading cycles, and set C is for a version having an intermediate reset

cycle. V_r and V_c are the voltages applied to a row conductor 18 and a column conductor 20 respectively and V_s is the resultant voltage appearing at a sense electrode 14. I_a and I_b are the consequential currents flowing in the column conductor 20 for comparatively low and high capacitances C_x respectively. It will be appreciated that the particular voltages referred to in Figure 6 are by way of example only.

Various modifications to the sensing device are possible. In the above-described embodiment, the sensing surface, 34, is provided solely by the exposed surface of the insulating film 32. Figures 7a and 7b show, in schematic plan view, different forms of the sensing device in which metal film conductors 53 are deposited directly on the exposed surface of the film 32 overlying the spaces between adjacent rows and columns of sense electrodes 14, either in the form of a grid conductor pattern, Figure 7a, or as linear conductors, Figure 7b. In operation these conductor patterns are grounded to improve the electrical contact to the finger surface.

Referring now to Figure 8 there is shown a schematic cross-section view, similar to Figure 3, through part of another embodiment of sensing device. This embodiment includes a further array of electrodes provided on the surface 34 of the insulating film 32. This array consists of discrete, electrically-isolated, conducting pads 54 approximately the same size and shape as, and substantially overlying, the sense electrodes 14. These pads 54 form, together with the electrodes 14, the opposite plates of the capacitors 35. Apart from the pads 54, this embodiment is the same as the previously-described embodiment and operation of the active matrix array of sense elements is generally identical. In use, a finger is placed over the array of pads 54 on the surface 34. Ridges of the fingerprint then contact and ground particular pads 54 of the array whereby the capacitance of the capacitors 35 at the sense elements concerned is determined by opposing electrodes 14 and 54 and the thickness of the film 32. Compared with the previous embodiment, substantially identical and more distinctive capacitances are obtained at all ridge contact locations. Elsewhere, surface portions of the finger are spaced from their underlying pads 54 and the capacitance values are dependent on this spacing as before. The capacitance variation across the array is thus indicative of the fingerprint's surface topology.

With regard to all the above-described sensing devices the FETs 16, as previously mentioned, may comprise amorphous silicon or polycrystalline silicon TFTs fabricated using standard processes known in the art of display devices. It is envisaged though that the array of FETs 16 could instead

form part of a conventional semiconductor integrated circuit using, for example, a silicon wafer substrate. However, using TFTs on an insulating substrate is considered preferable because any stray capacitance to the sense electrodes is then minimised. In addition to achieving low stray capacitance, TFT on glass/quartz technology offers the further benefit of the ability to provide relatively large area devices, around for example 30mm by 40mm at comparatively low cost.

In the case where polycrystalline silicon is used, the addressing circuits 22 and 24 may conveniently be formed simultaneously with the FETs 16 at the periphery of the substrate 30 so as to be integrated with the active sensing matrix and provide a very compact sensing device.

Figure 9 illustrates in schematic block form a fingerprint recognition system employing a sensing device as described previously, here referenced at 60. The signal output from the sensing device 60 is in a form akin to the video type output provided by known optical type fingerprint sensing devices using image sensors. Accordingly, it will be appreciated that apart from the sensing device the components of the system generally follow conventional practice as will be apparent to persons skilled in the art and it is considered unnecessary therefore to describe these components in detail. Briefly, the output from the device 60, suitably conditioned, is fed to an analysis circuit 61 which is programmed to detect characterising features of the fingerprint sensed such as the position of minutiae. Data from the circuit 61 is supplied to a computer 62 which through standard algorithms compares the data with characteristical data of a plurality of fingerprints, or a single fingerprint depending on whether the system is used for identification or merely verification purposes, held in a storage device 63 and which provides an output in accordance with whether or not a match has been found.

The circuit 61 can be programmed either to utilise the three dimensional information provided by the sensing device for high accuracy of recognition, or alternatively, with appropriate discrimination to select particular output signal values from the device 60, utilising specific information representative of the two dimensional ridge pattern in the nature of a binary image similar to that obtained from known optical sensing devices. From reading the present disclosure, other modifications will be apparent to persons skilled in the art. Such modifications may involve other features which are already known in the art of fingerprint recognition and which may be used instead of or in addition to features already described herein.

Claims

1. A fingerprint sensing device comprising an array of sense elements which are connected to a drive circuit and which each comprise a sense electrode, the sense electrodes being covered by dielectric material determining a sensing surface over which a finger whose print is to be sensed is placed, characterised in that each sense element includes a respective switching device connected to its sense electrode and is actively addressable by means of the drive circuit controlling operation of the switching device to apply a predetermined potential to the sense electrode, and in that the sensing device further includes sensing means for sensing a capacitance produced by individual finger surface portions in combination with respective sense electrodes when a finger is placed over said sensing surface.
2. A fingerprint sensing device according to Claim 1, characterised in that the drive circuit is arranged to apply said predetermined potential to each sense electrode at regular intervals, the predetermined potential being different to the potential present on the sense electrode at the time said predetermined potential is applied.
3. A fingerprint sensing device according to Claim 1 or Claim 2, characterised in that the sensing means is operable to sense the charging current to a sense electrode upon application thereto of the predetermined potential and to provide a signal output in accordance therewith.
4. A fingerprint sensing device according to Claim 3, characterised in that the sensing means comprises charge sensing amplifiers.
5. A device according to Claim 3, characterised in that the sensing means comprises current sensing amplifiers.
6. A fingerprint sensing device according to any one of the previous claims, characterised in that the switching device of each sense element comprises a transistor whose terminals are connected respectively to the sense electrode of the sense element and to the drive circuit by a sense conductor via which the predetermined potential is applied and whose control electrode is connected by an address conductor to the drive circuit via which a select signal is supplied from the drive circuit to control the transistor.
5. A fingerprint sensing device according to Claim 6, characterised in that the sense elements are in a row and column array and in that each row and column of sense elements are connected respectively to a common address conductor and a common sense conductor.
10. A fingerprint sensing device according to Claim 7, characterised in that the array of sense elements is addressed by the drive circuit one row at a time in sequence.
15. A fingerprint sensing device according to any one of Claims 6 to 8, characterised in that the transistors of the array of sense elements comprise thin film transistors which together with the address and sense conductors and the sense electrodes are provided on an insulating substrate.
20. A fingerprint sensing device according to any one of the preceding claims, characterised in that the dielectric material covering the sense electrodes provides an exposed surface onto which a finger whose print is to be sensed is placed.
25. A fingerprint sensing device according to any one of Claims 1 to 9, characterised in that an array of discrete electrodes is provided on the dielectric material opposite to the sense electrodes, each of which substantially overlies a respective sense electrode.
30. A fingerprint sensing device according to Claim 10 or 11, characterised in that the dielectric material extends as a common and continuous layer over all sense electrodes of the array.
35. A fingerprint sensing device according to any one of Claims 1 to 9, characterised in that the dielectric material comprises a layer extending over all the sense electrodes of the array and having a continuous surface remote from the sense electrodes and in that further conductors are provided on said continuous surface which extend over regions between the sense electrodes and which, in operation of the device, are grounded.
40. A fingerprint recognition system comprising a fingerprint sensing device according to any one of Claims 1 to 13, means responsive to an output from the capacitance sensing means of the device to provide characteristical data of a sensed fingerprint, and means for comparing

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said characteristical data with stored characteristical data for one or more fingerprints.

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Fig.1.

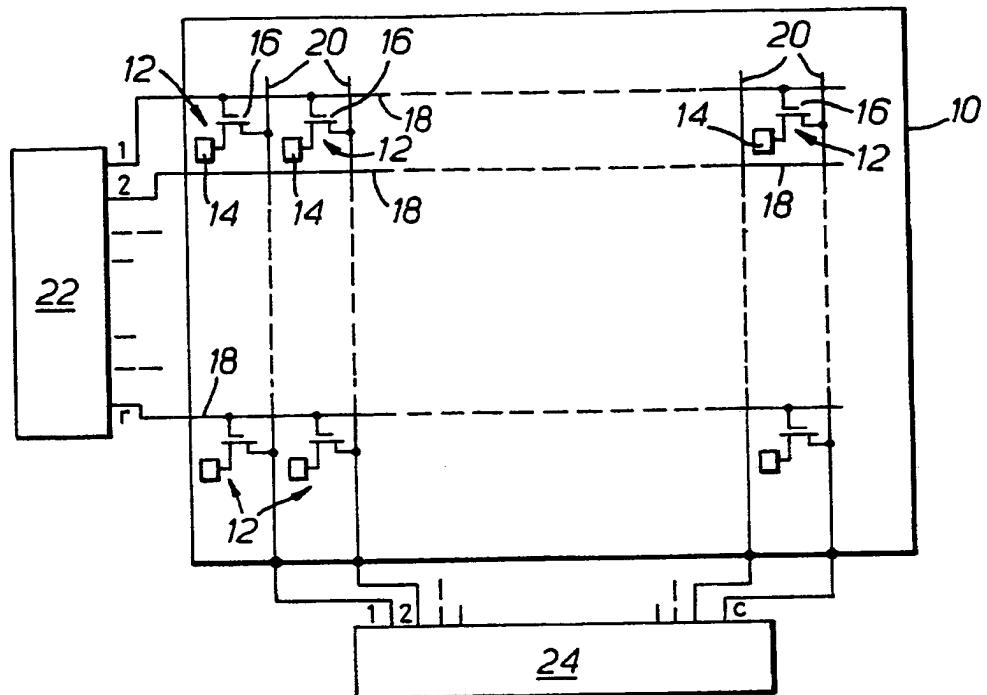


Fig.2.

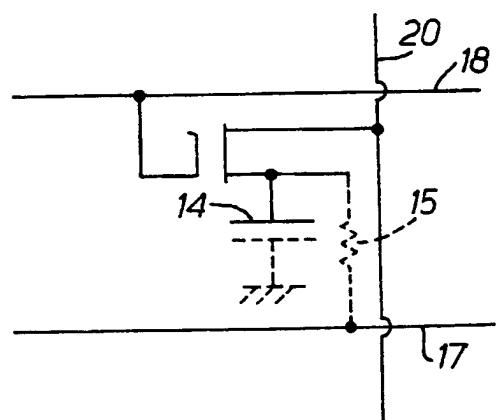


Fig. 3.

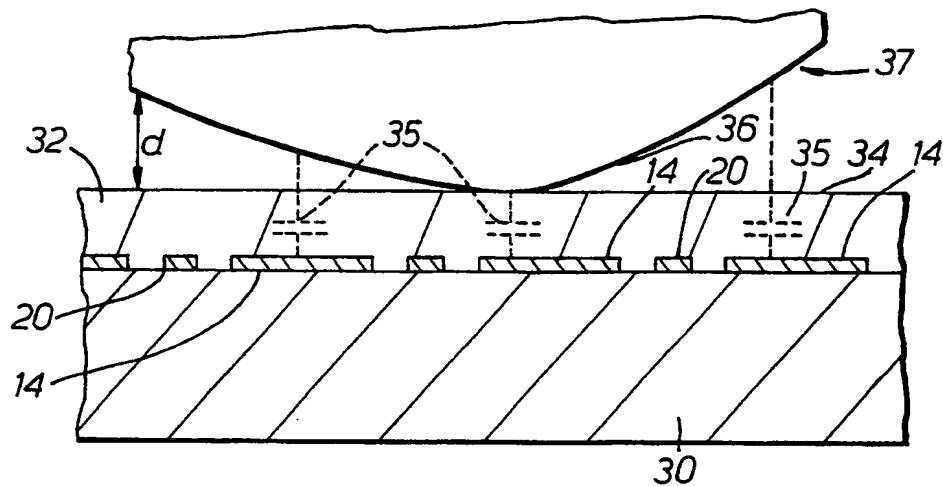


Fig. 4.

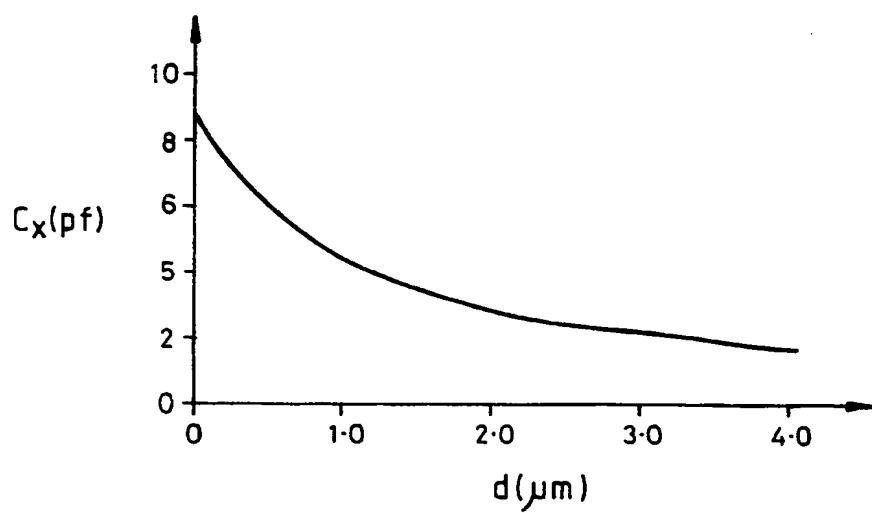


Fig.5.

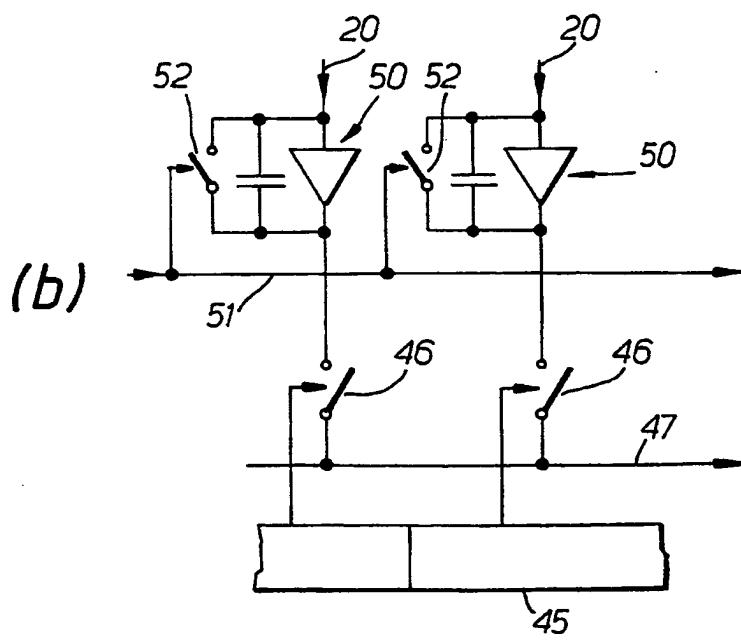
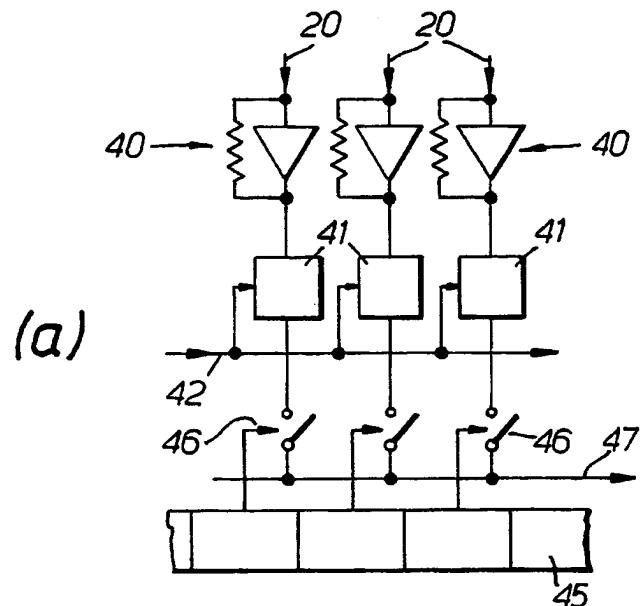


Fig.6.

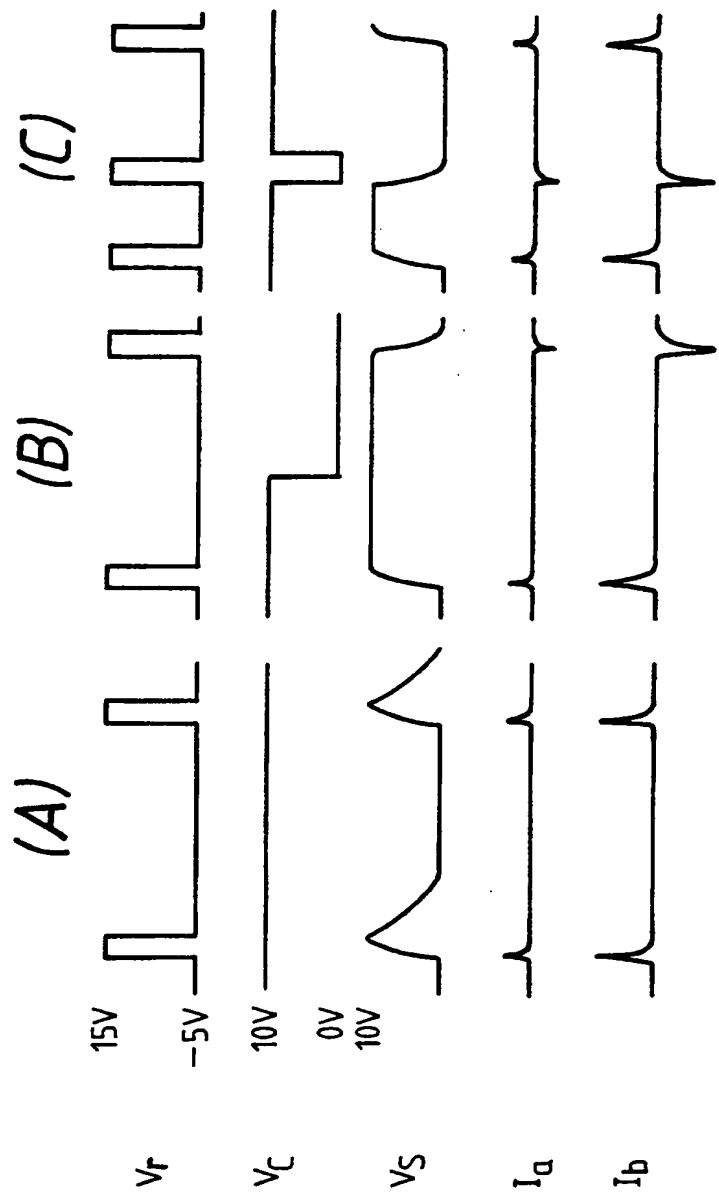


Fig.7.

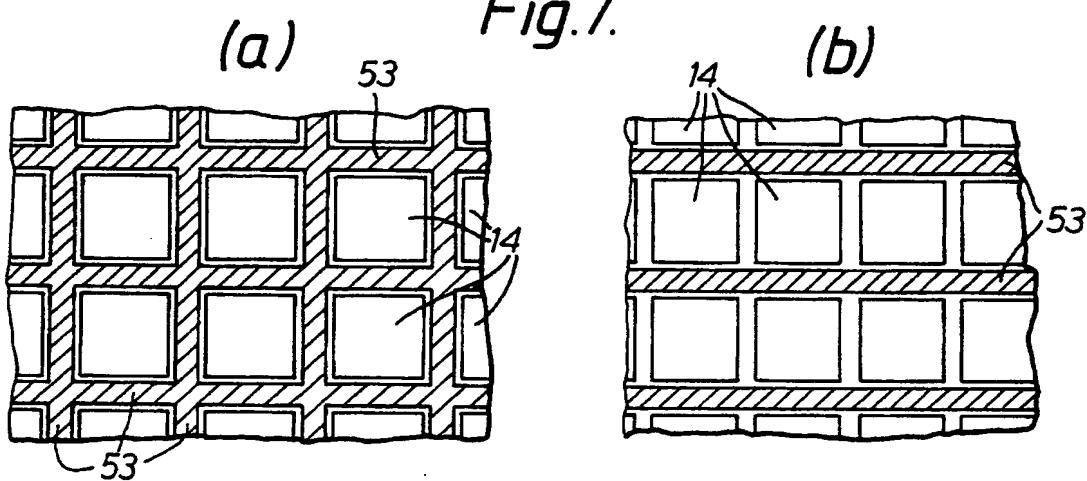


Fig.8.

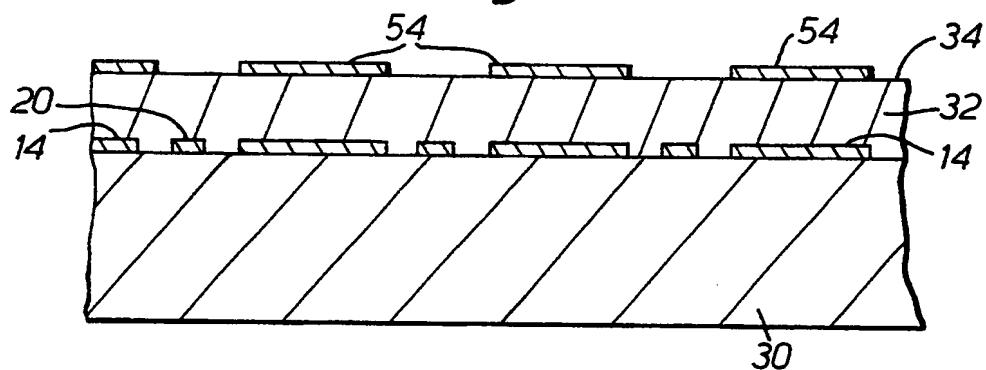
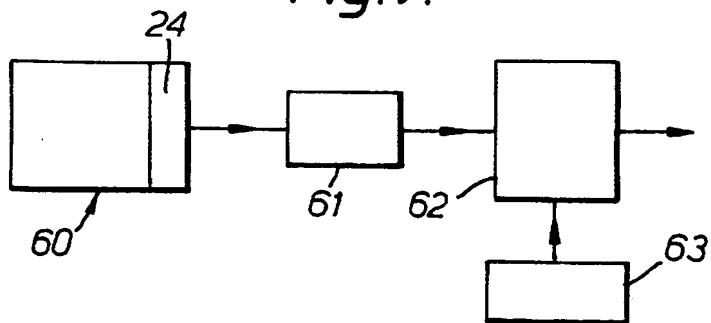


Fig.9.



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